

Model Name:GA-G41MT-S2PT

Revision 1.0

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SHEET

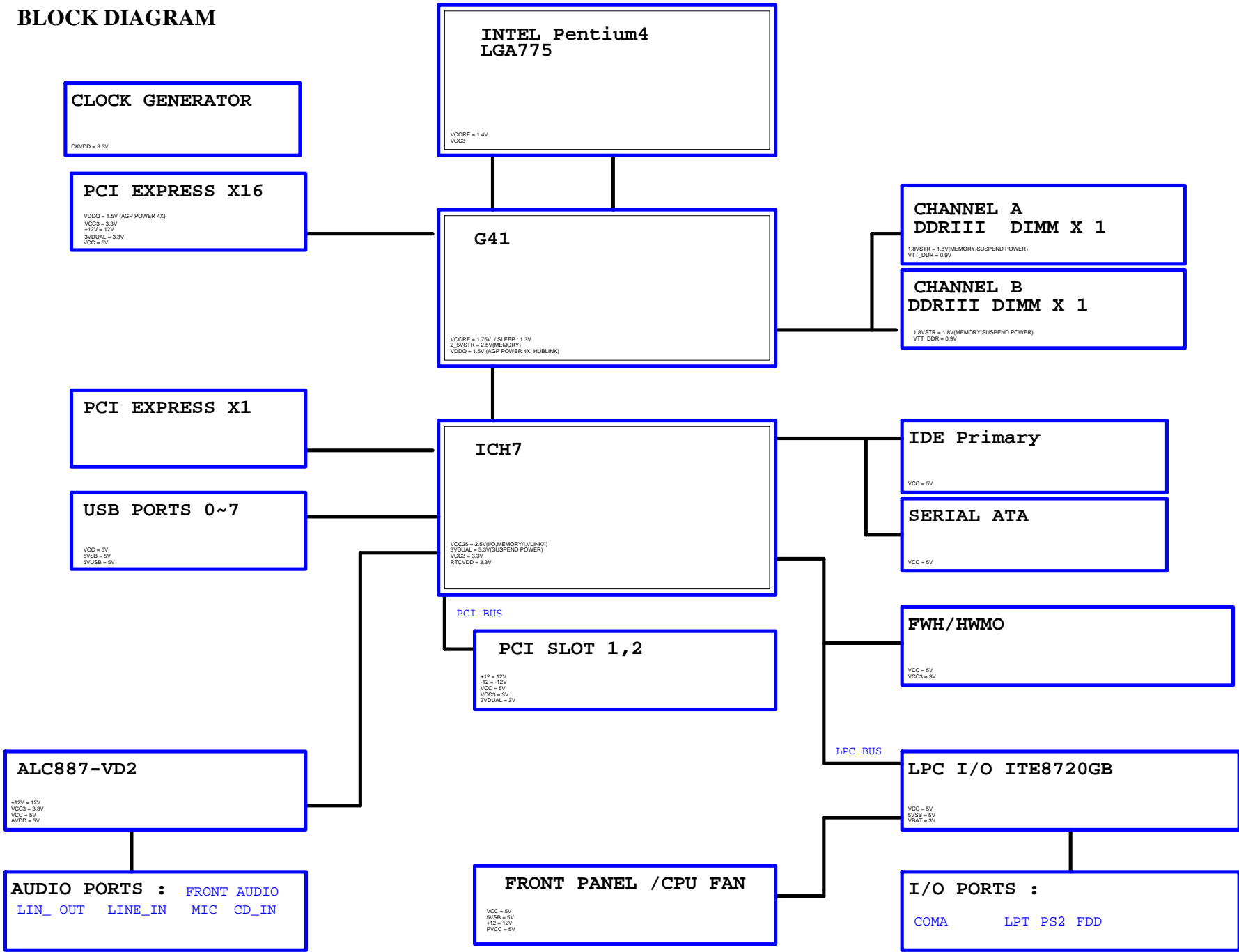
TITLE

28	REAR AUDIO JACK
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BLOCK DIAGRAM

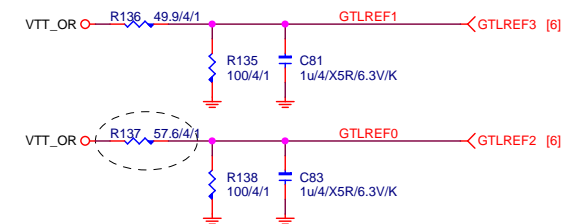
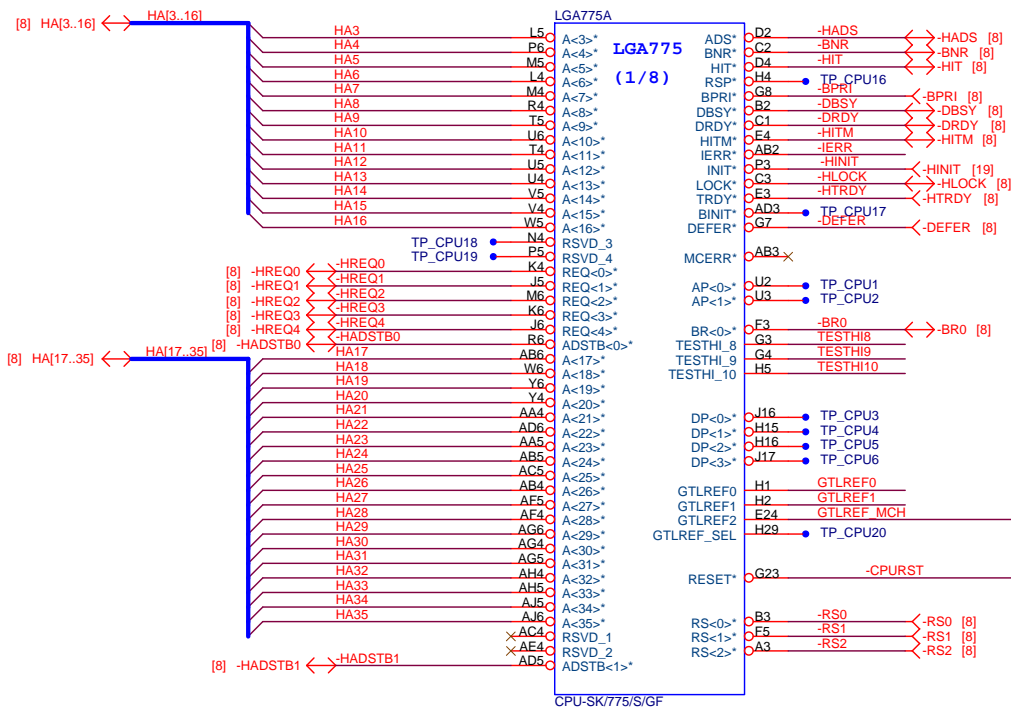
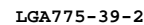
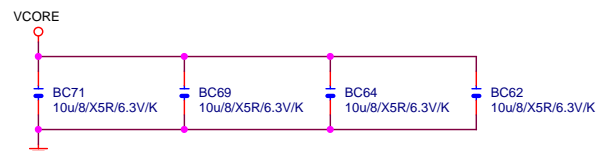
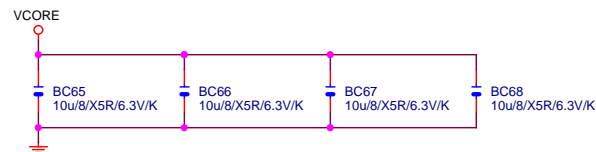


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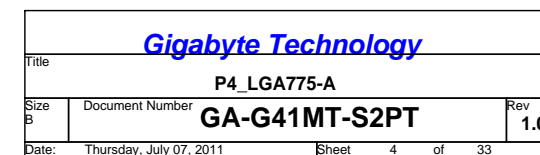
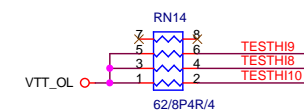
2011/07/05

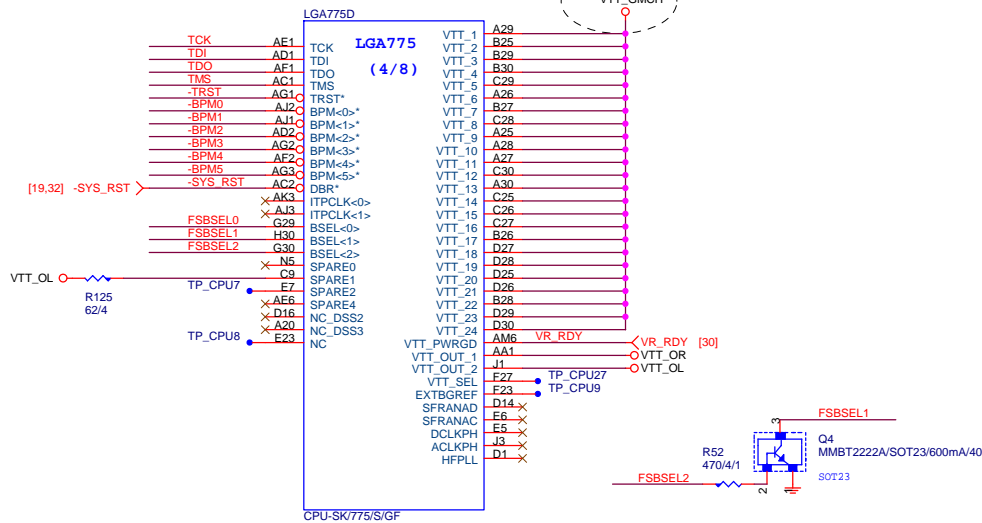
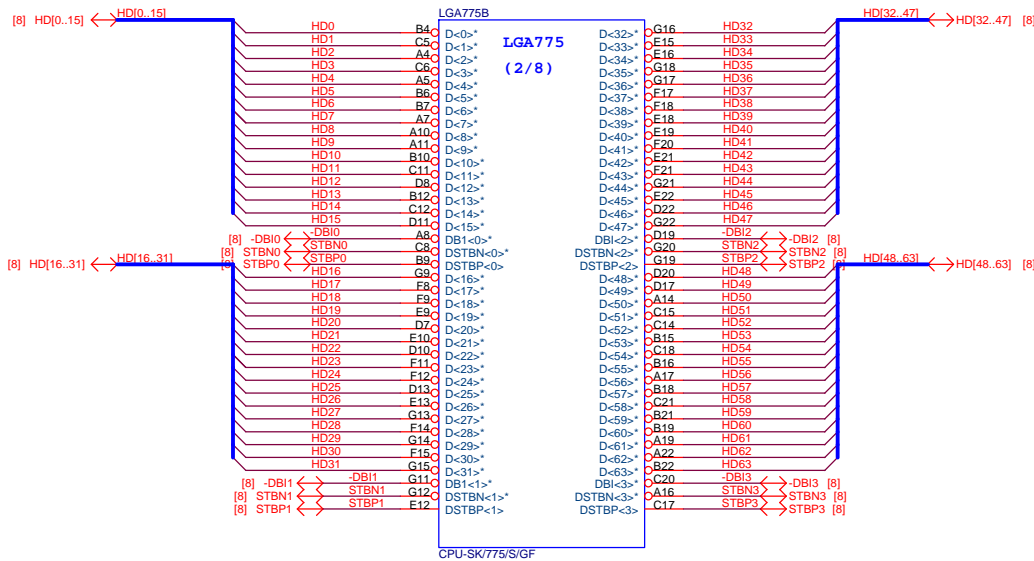
[illegible][illegible]

<i>Gigabyte Technology</i>			
BOM & PCB MODIFY HISTORY			
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中間値0.9V

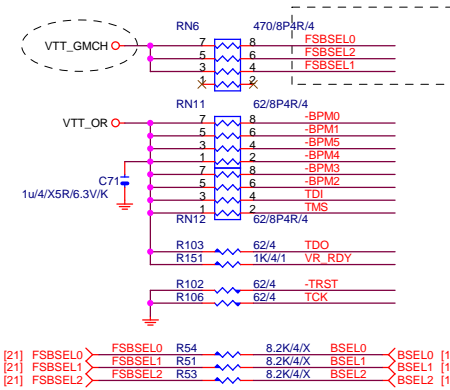




FIX FSB1600 LATCH FAIL

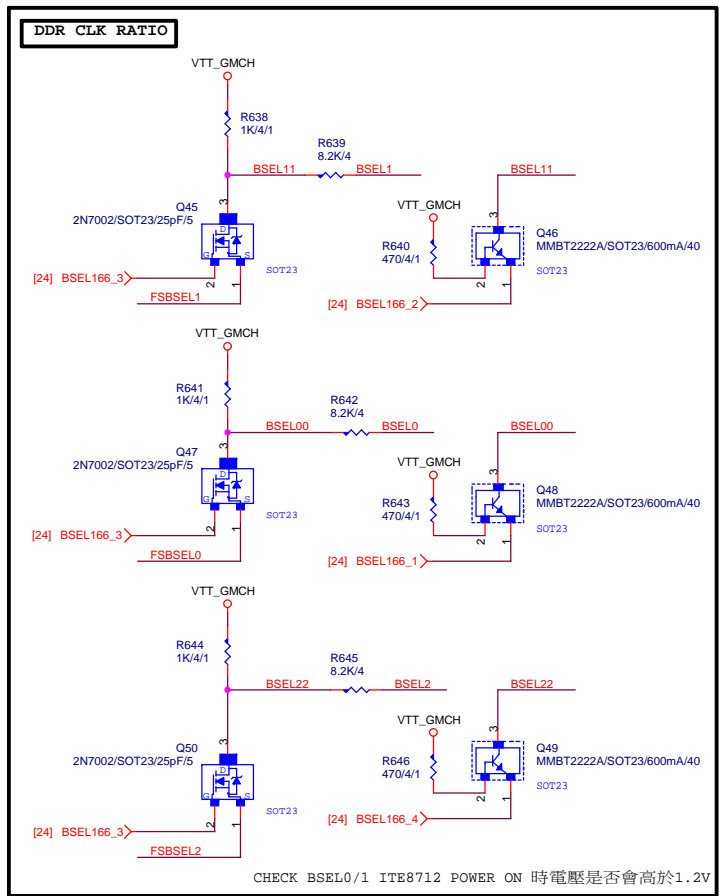
	FSA	FSB	FSC	
FSBSEL0	FSBSEL1	FSBSEL2	Clock	
?	1	0	1	100MHz
?	1	0	0	133MHz
G33	0	1	0	200MHz
G33	0	0	0	266MHz
G33	0	0	1	333MHz

400/533
400/533/667/800
533/667/800/1066
667/800/1066/1333



TO CLK GEN

TO NB

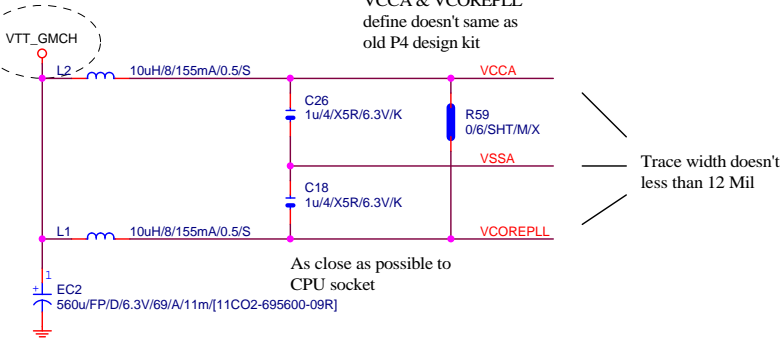


CHECK BSEL0/1 ITE8712 POWER ON 時電壓是否會高於1.2V

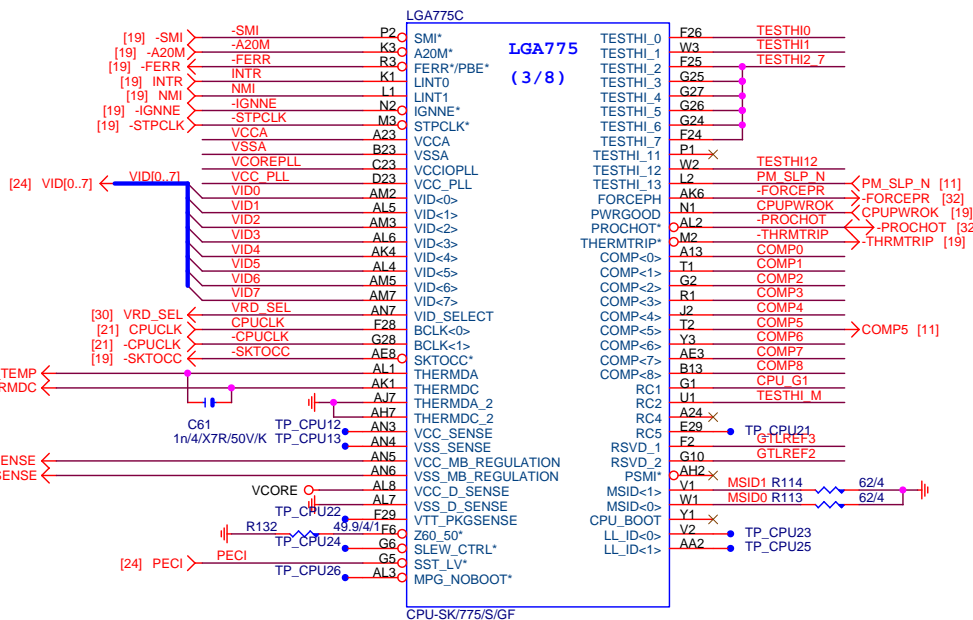
Gigabyte Technology

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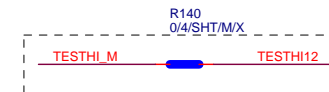
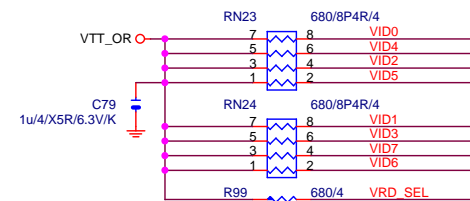
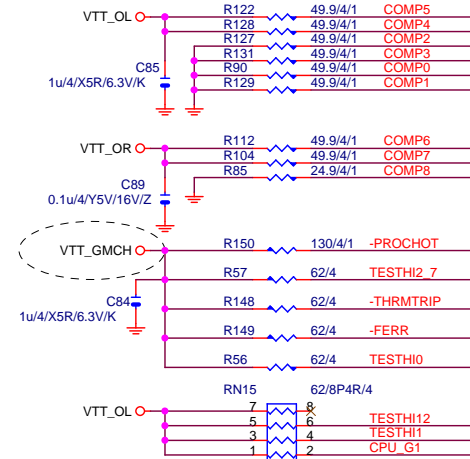
Note:
VCCA & VCOREPLL
define doesn't same as
old P4 design kit



As close as possible to
CPU socket



Place outside of CPU socket



GTLREF3 → GTLREF3 [4]

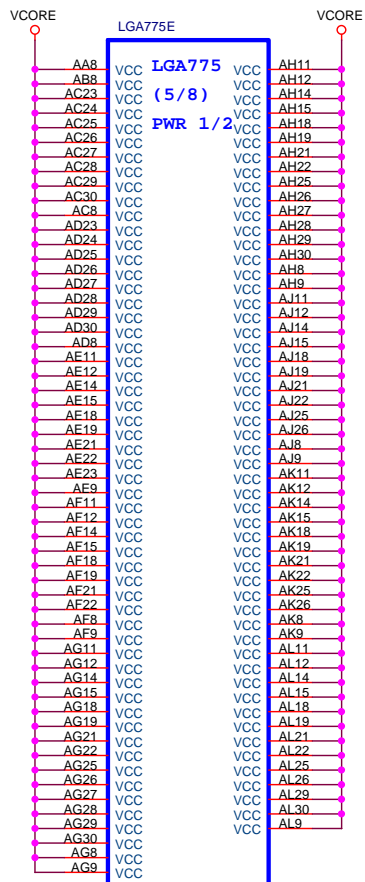
GTLREF2 → GTLREF2 [4]



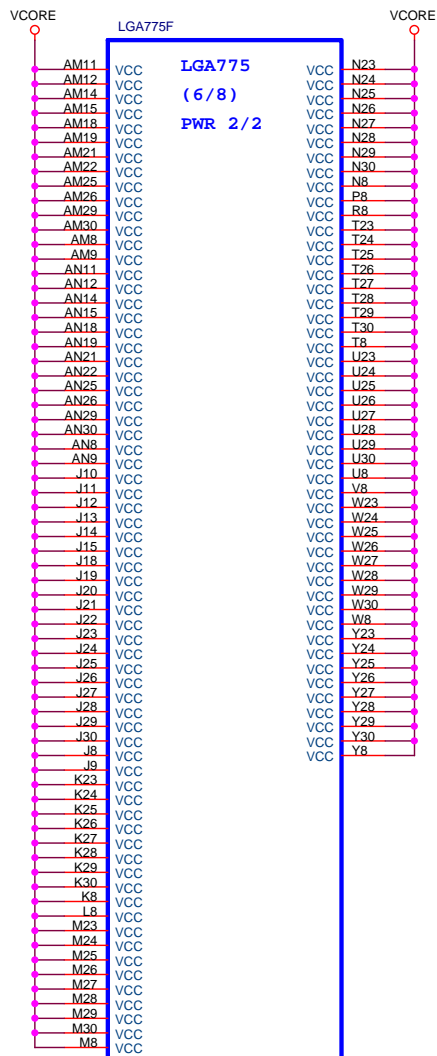
Gigabyte Technology

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B				1.0
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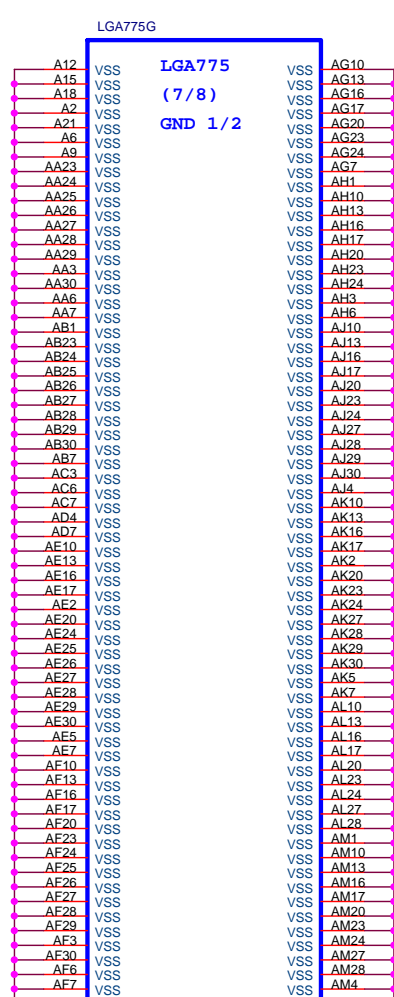
PECI:Platform Environment Control Interface



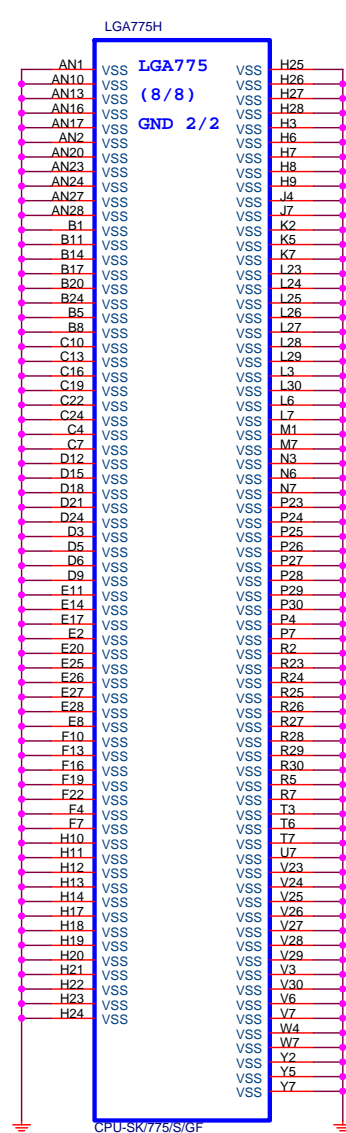
CPU-SK/775/S/GF



CPU-SK/775/S/GF



CPU-SK/775/S/GF



CPU-SK/775/S/GF

Gigabyte Technology

Title

P4_LGA775-E,F,G,H

Size

Document Number

GA-G41MT-S2PT

Rev

1.0

Date:

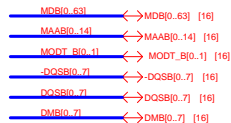
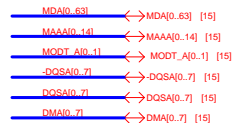
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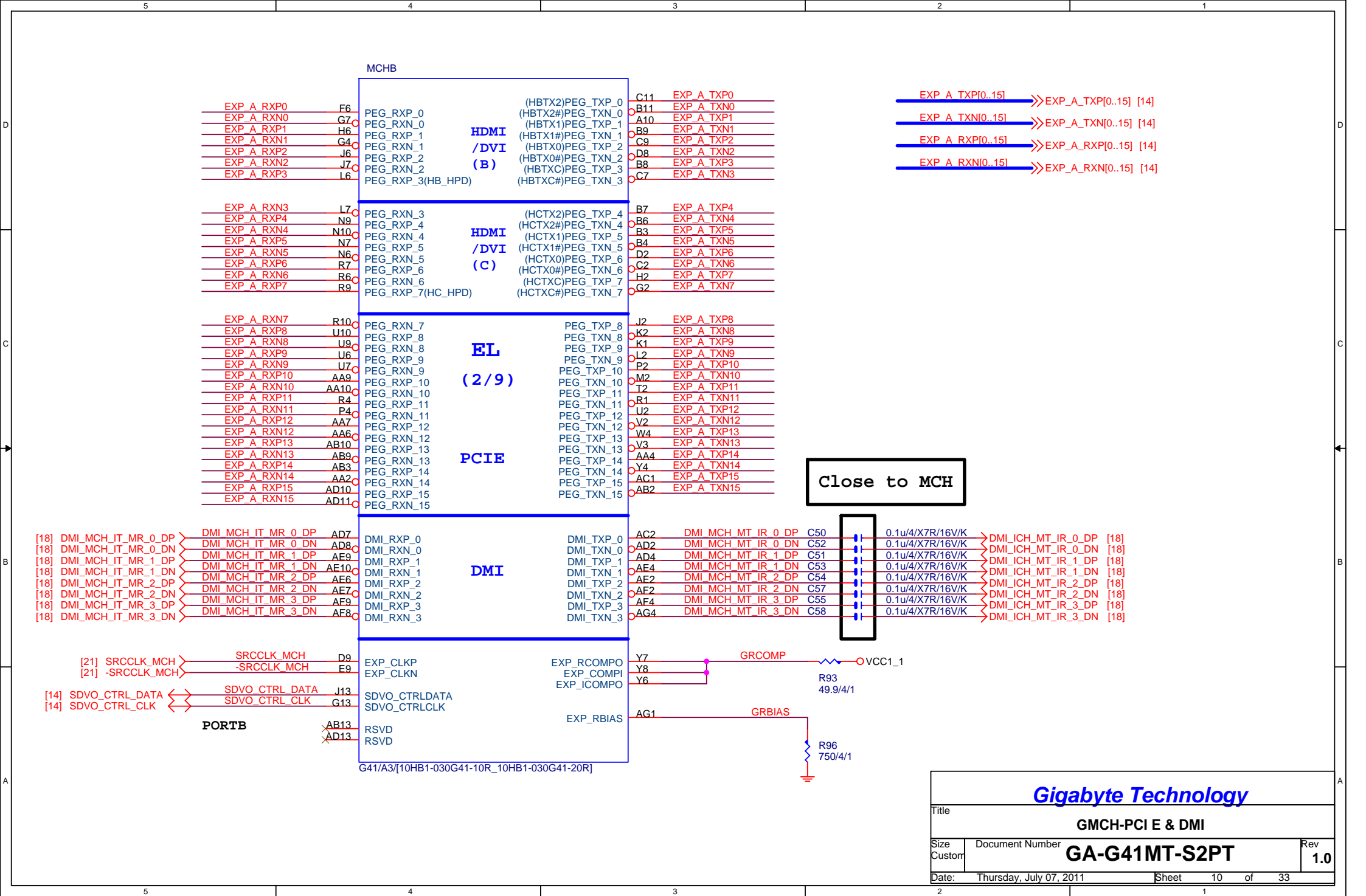
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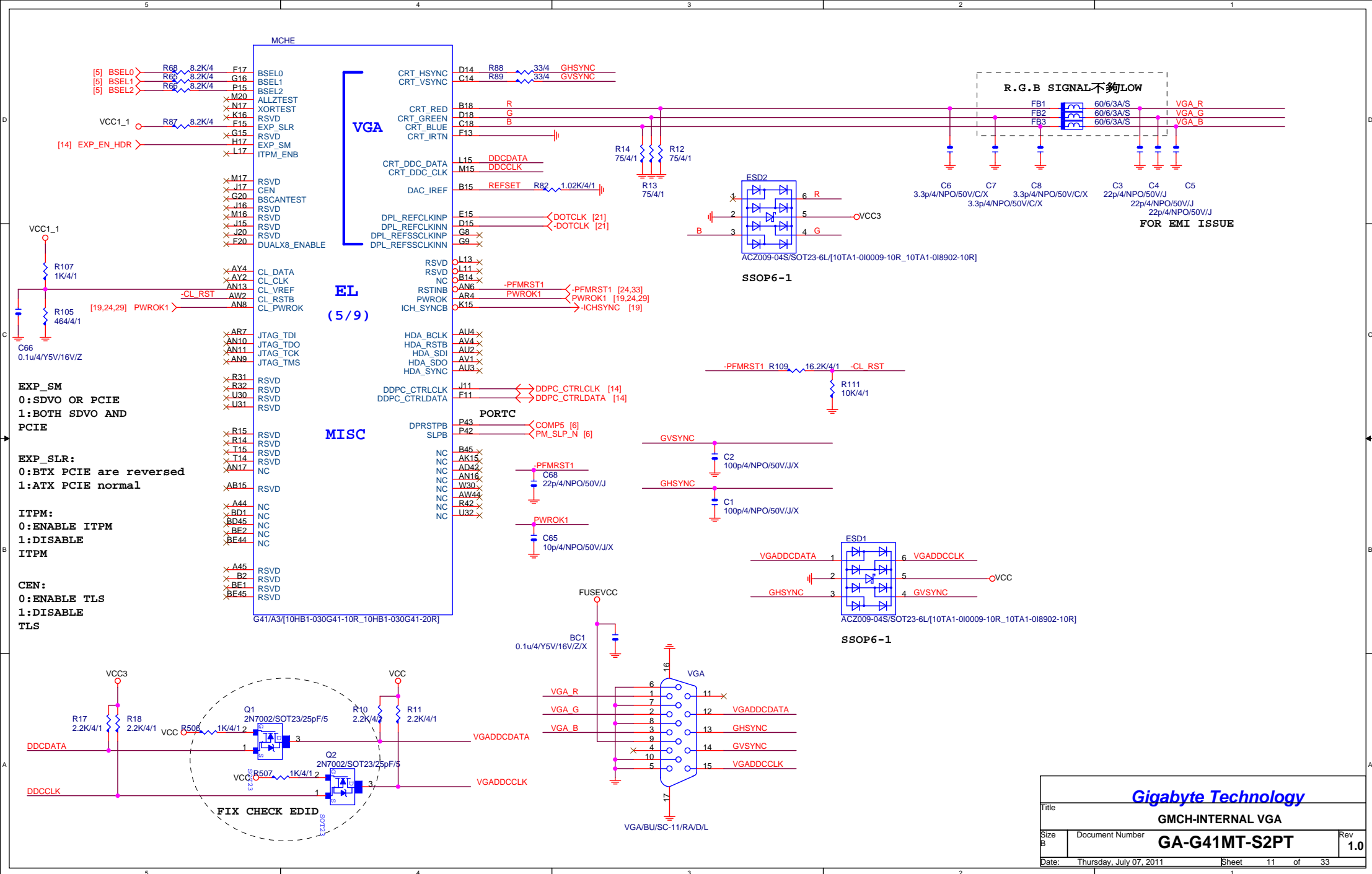


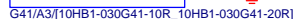
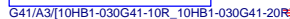
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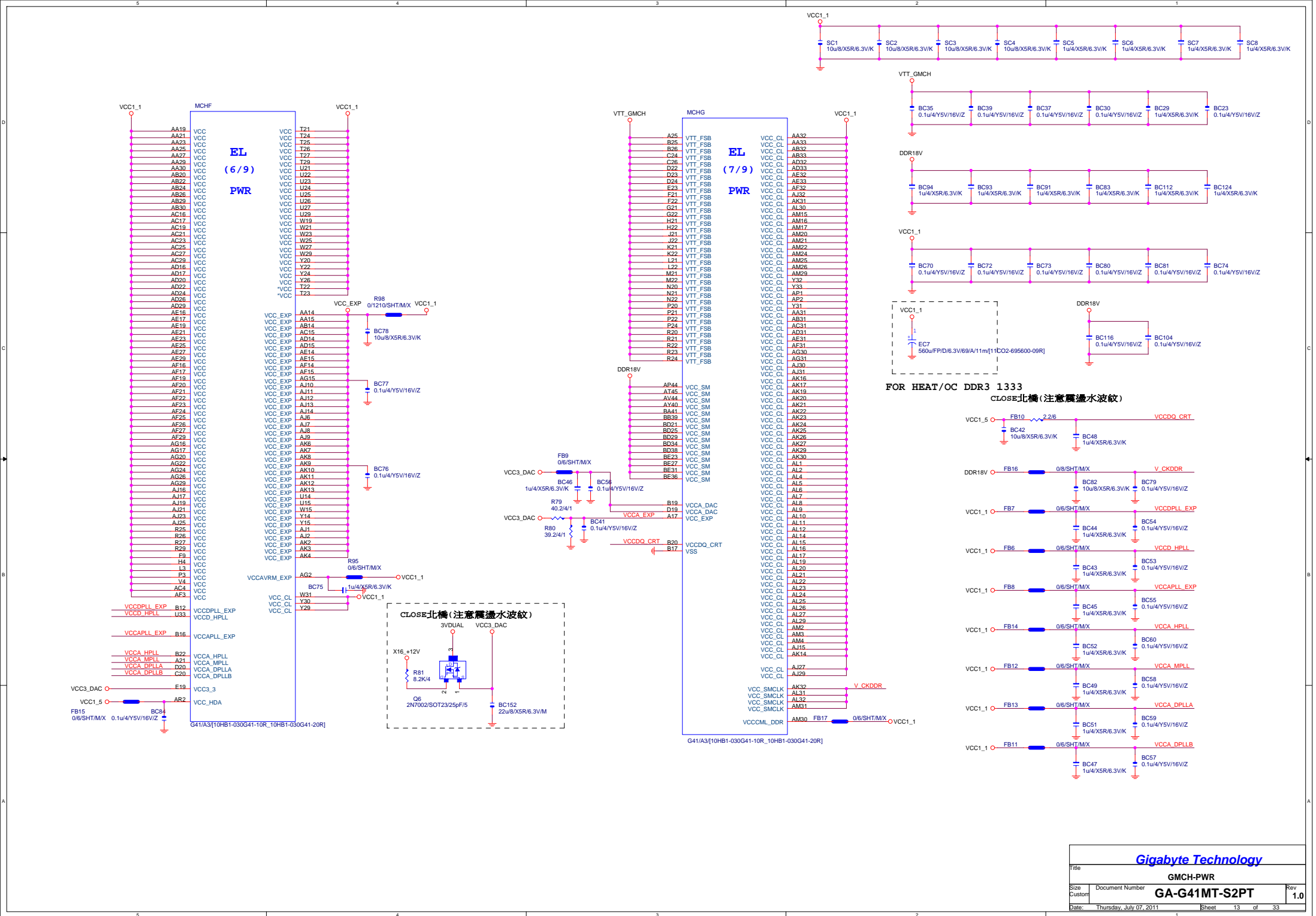
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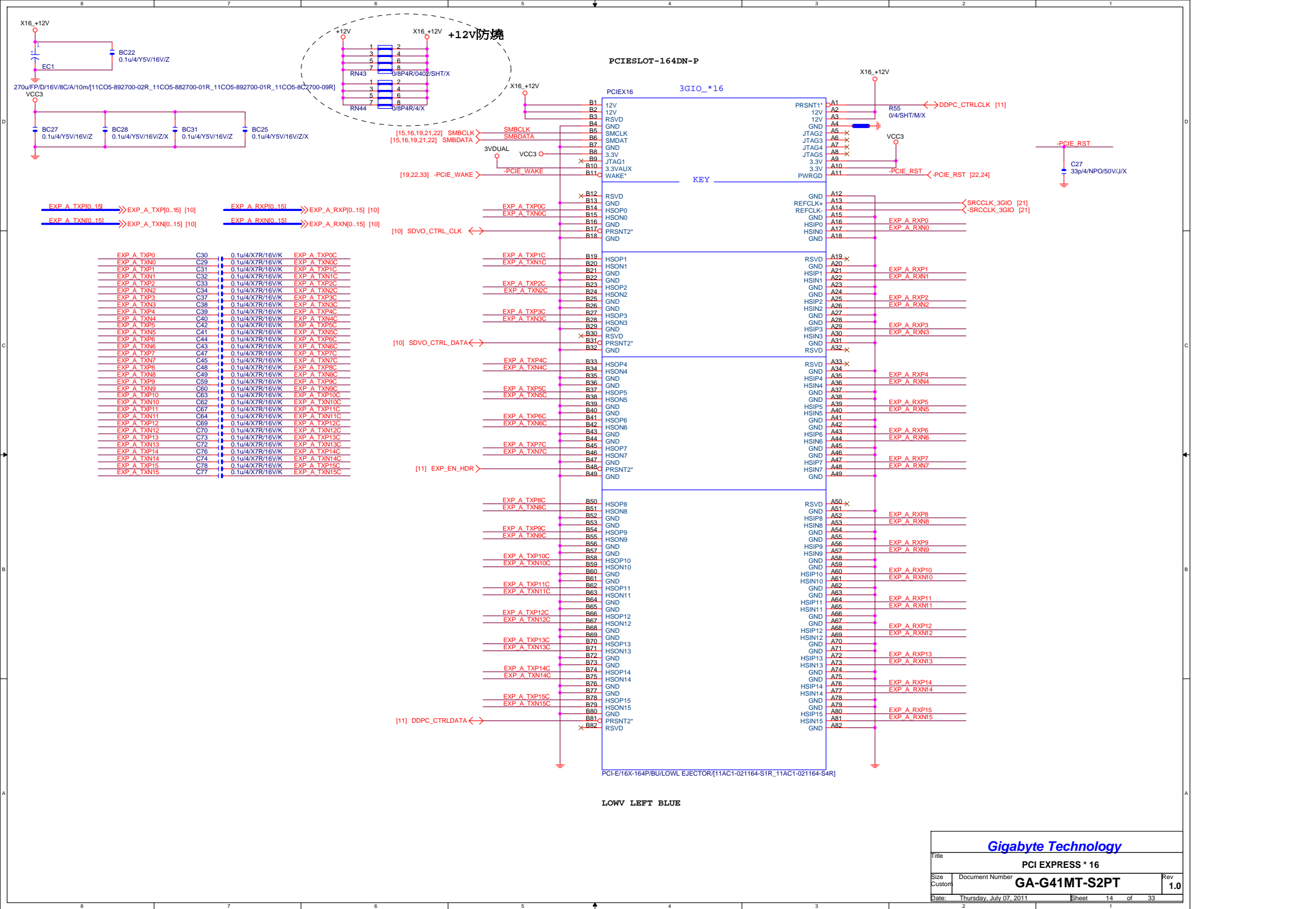


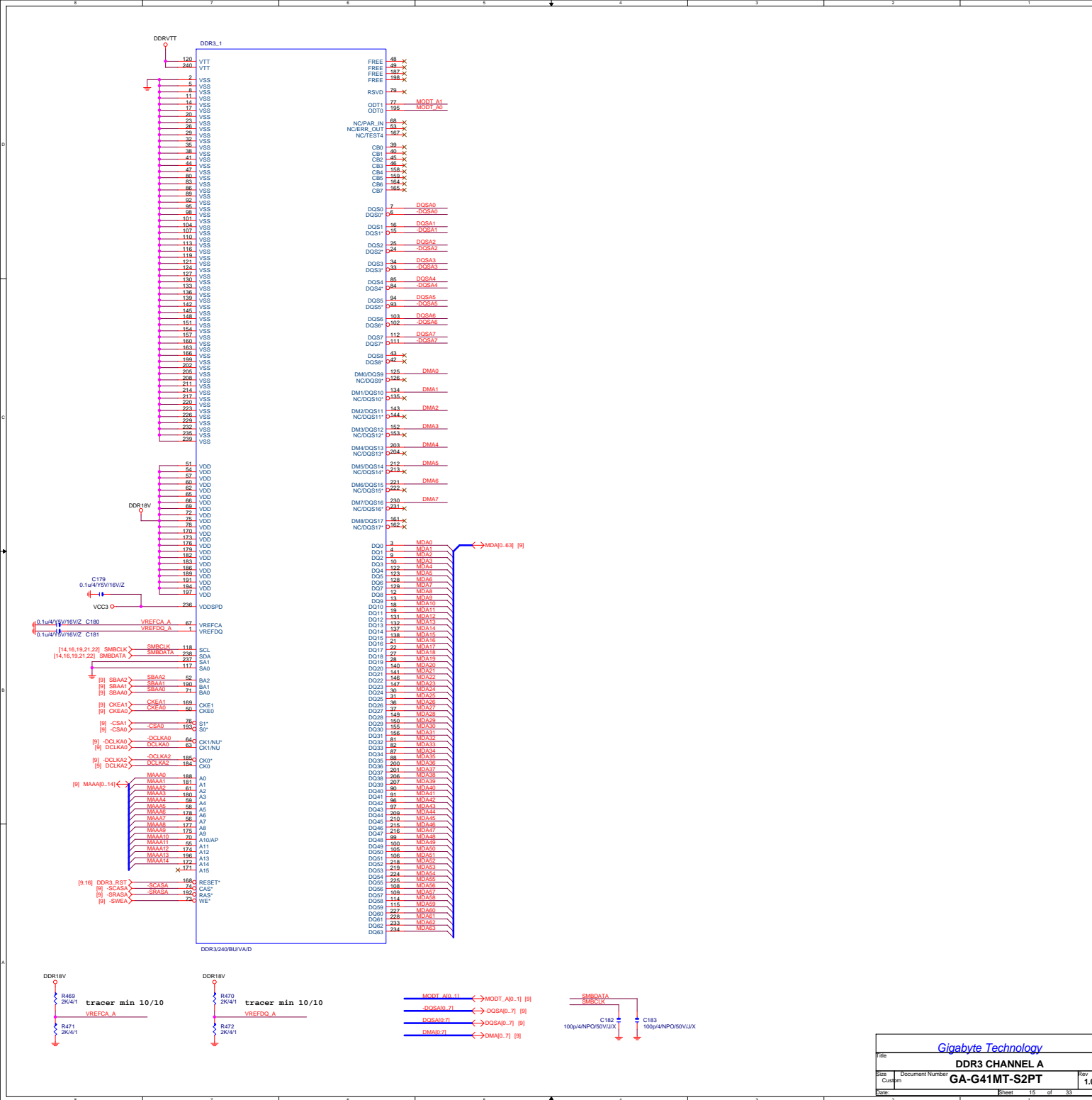


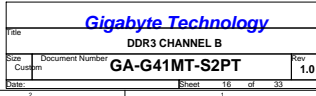
NEW COST DOWN

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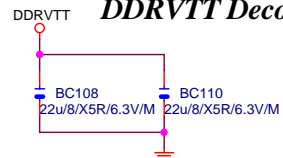


DDR TERMINATION CHANNEL A

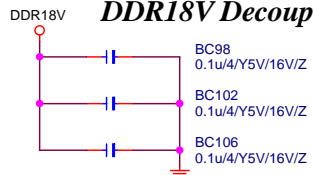
DDR18V Decouple



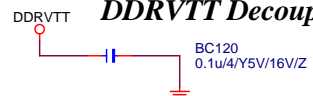
DDRVTT Decouple



DDR18V Decouple

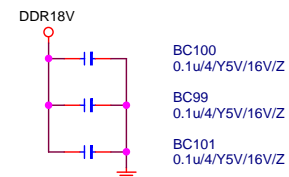


DDRVTT Decouple

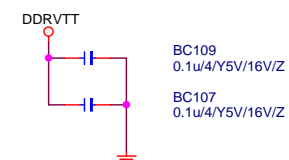


DDR TERMINATION CHANNEL B

DDR18V Decouple

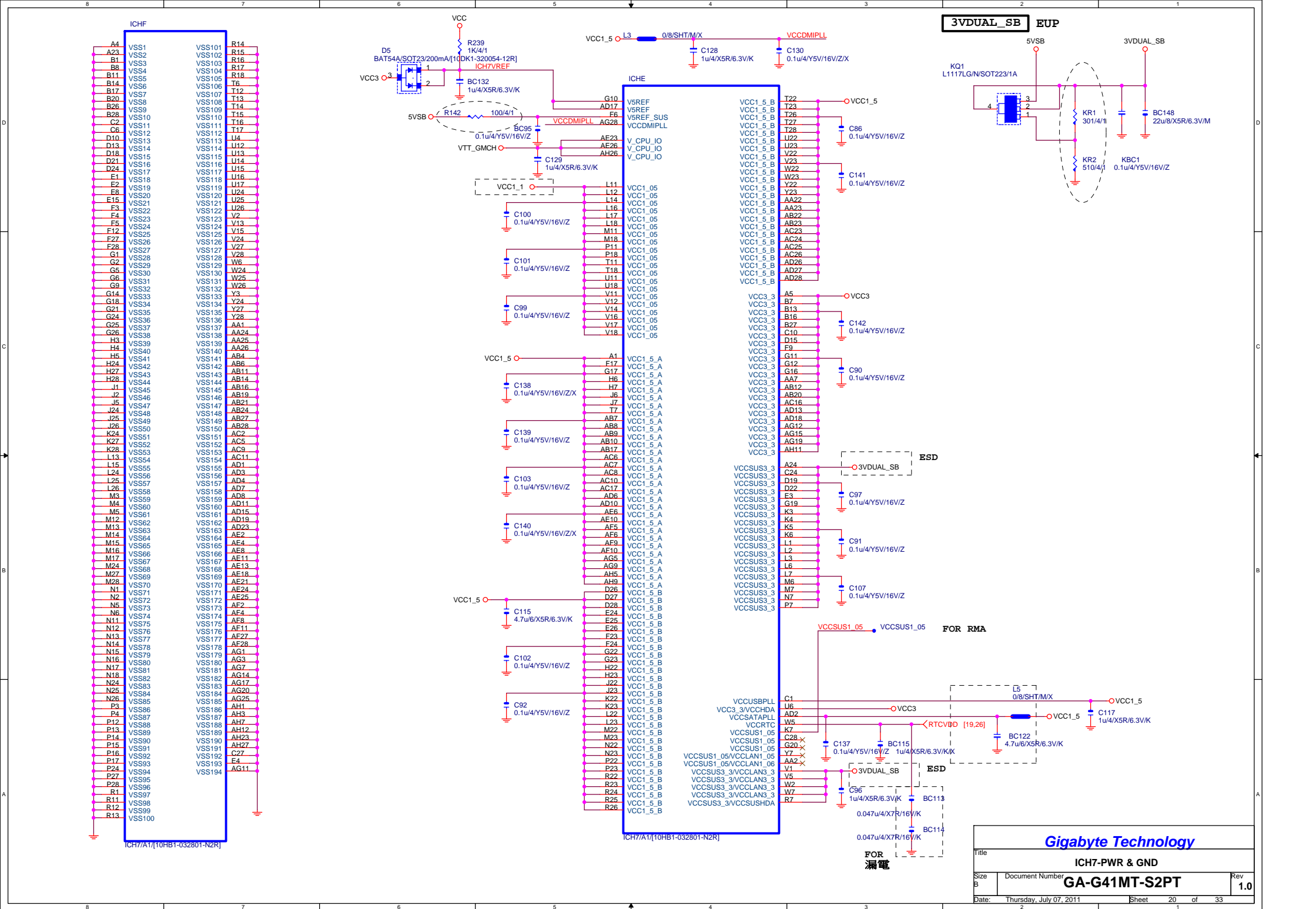


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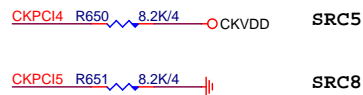
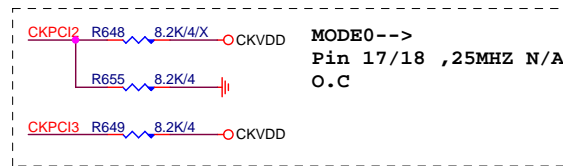
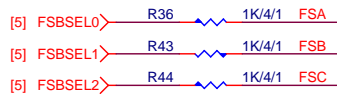
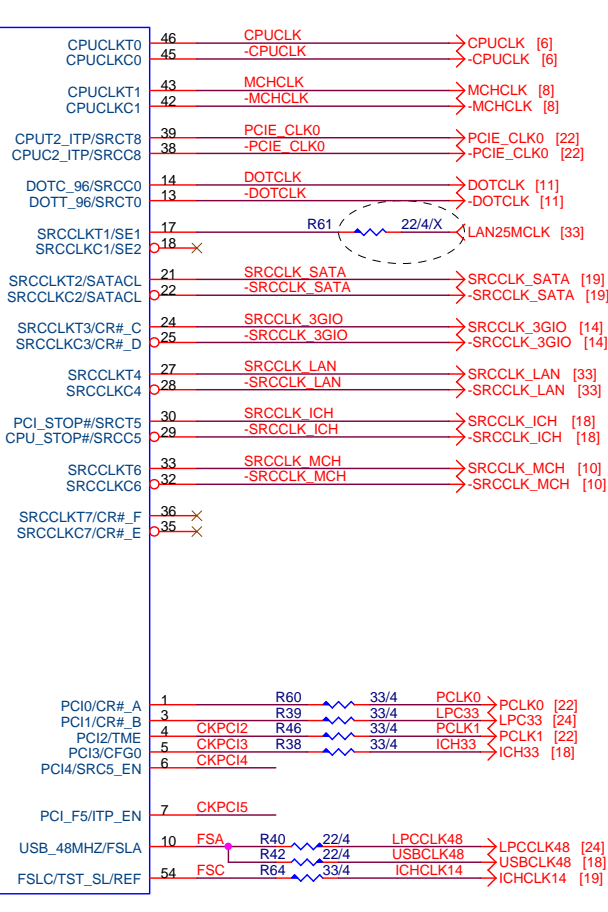
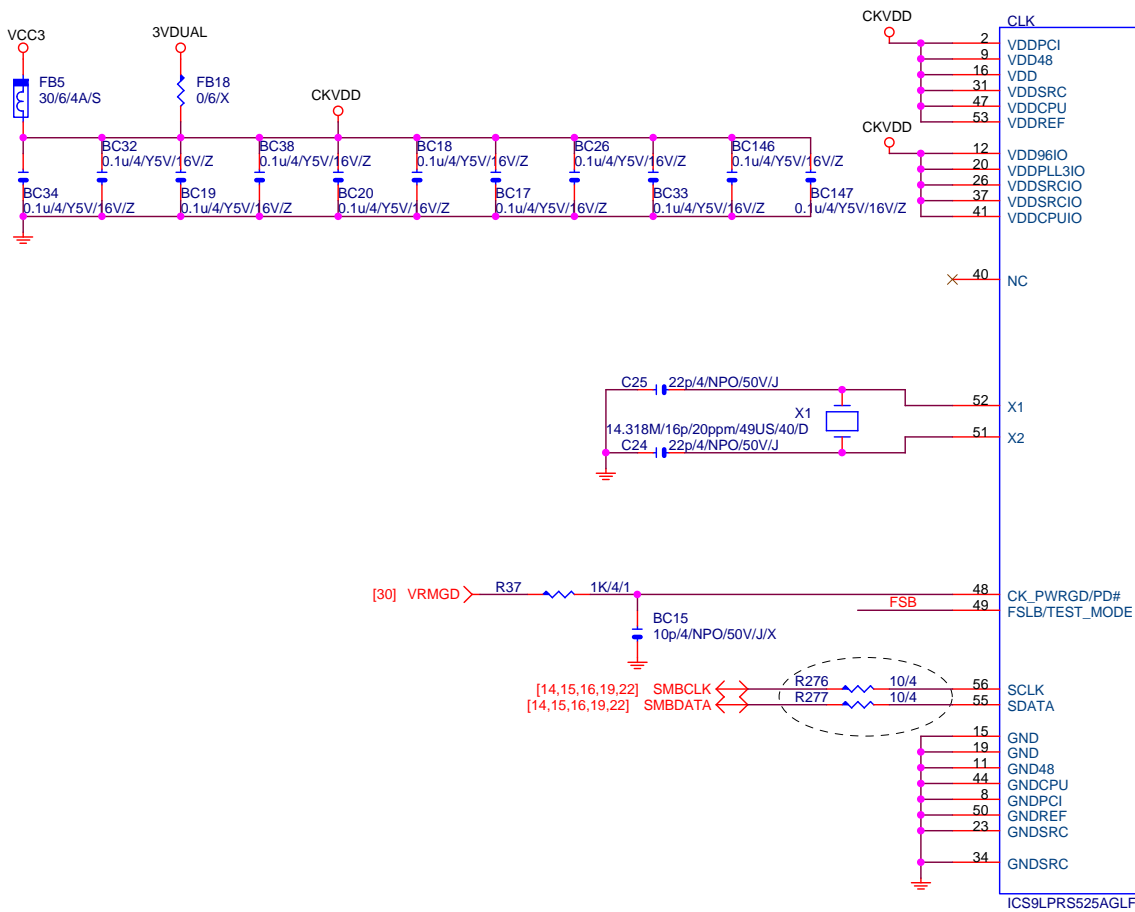


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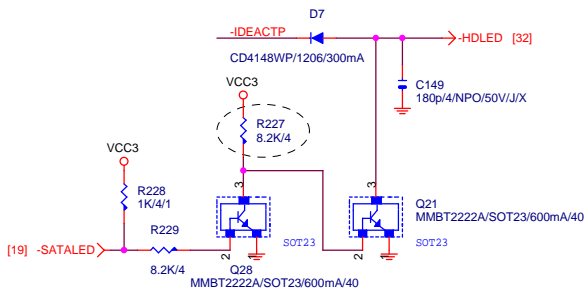
CLK GEN CK505



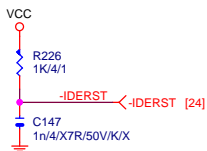
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Title			
CK505 CLK GEN			
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IDE/SATA LED



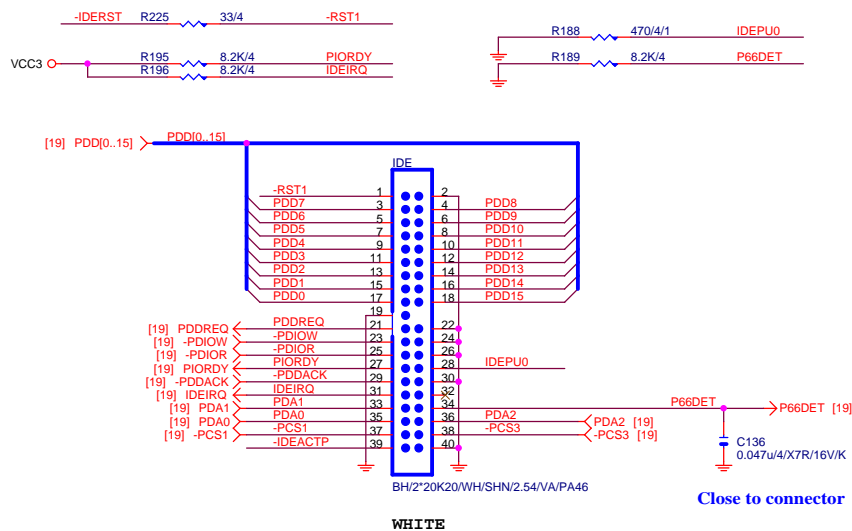
IDE RESET



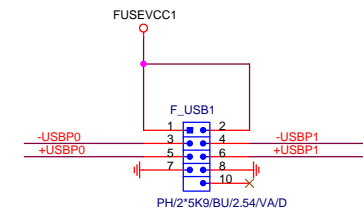
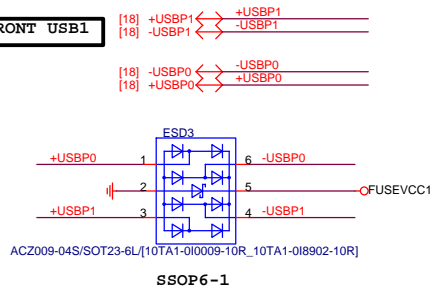
FLOPPY

N/A

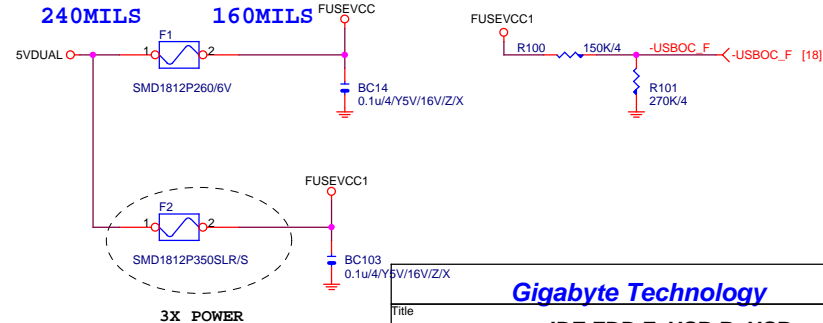
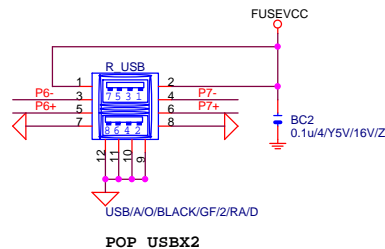
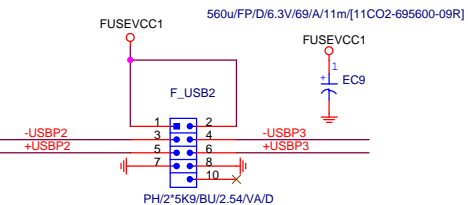
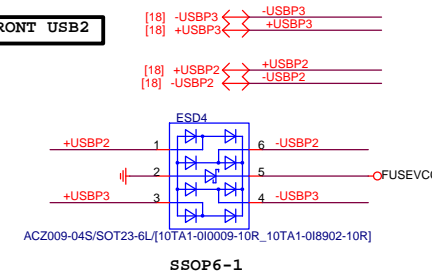
IDE



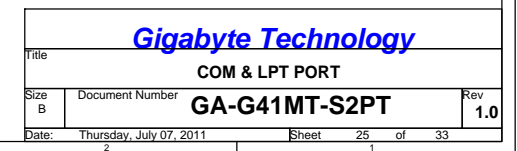
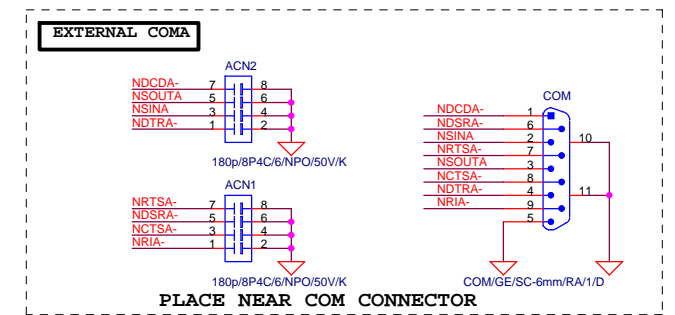
FRONT USB1



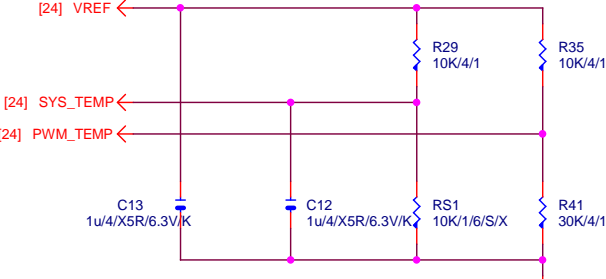
FRONT USB2



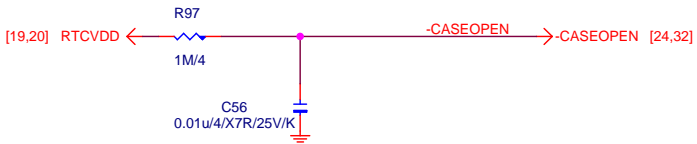
Gigabyte Technology		
IDE,FDD,F_USB,R_USB		
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TEMP H/W MONITOR

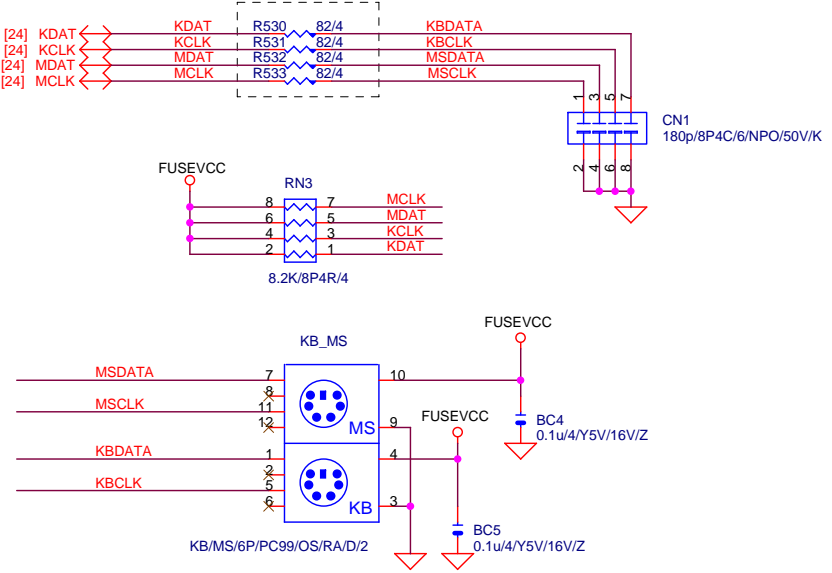


CASE OPEN

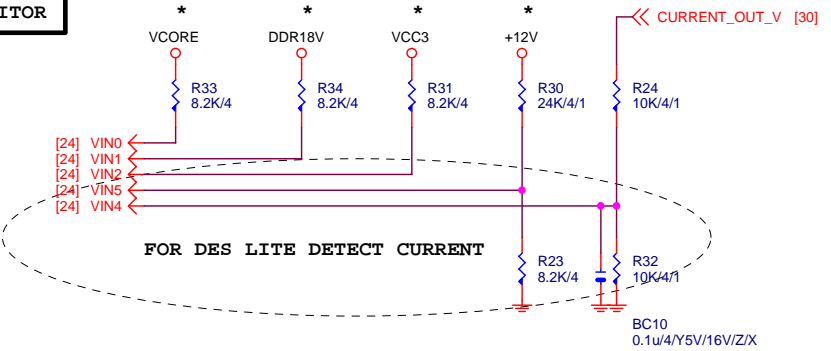


Case Open Circuits

KB/MS

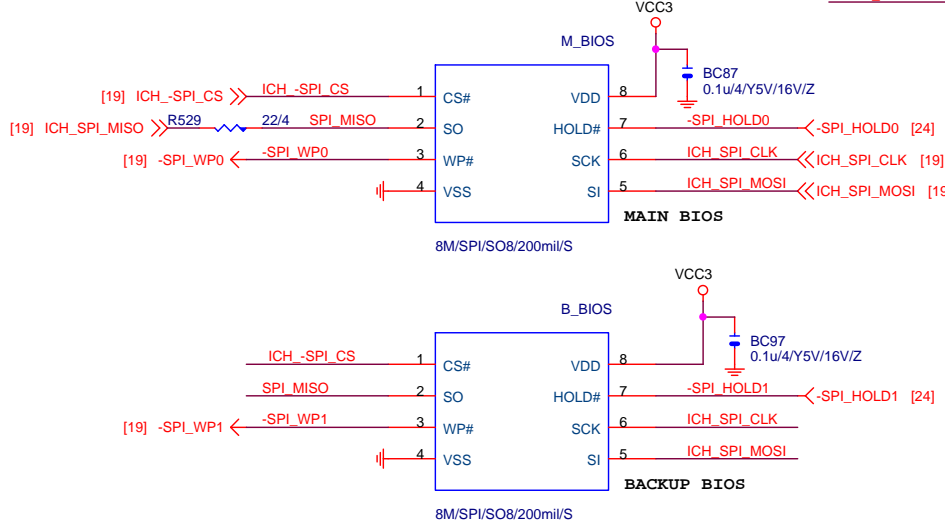
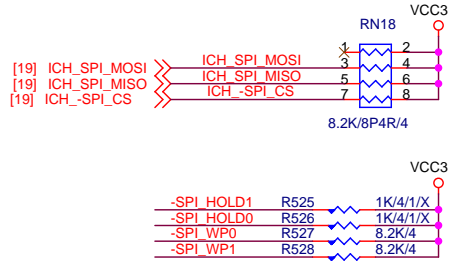


VOLTAGE-- H/W MONITOR



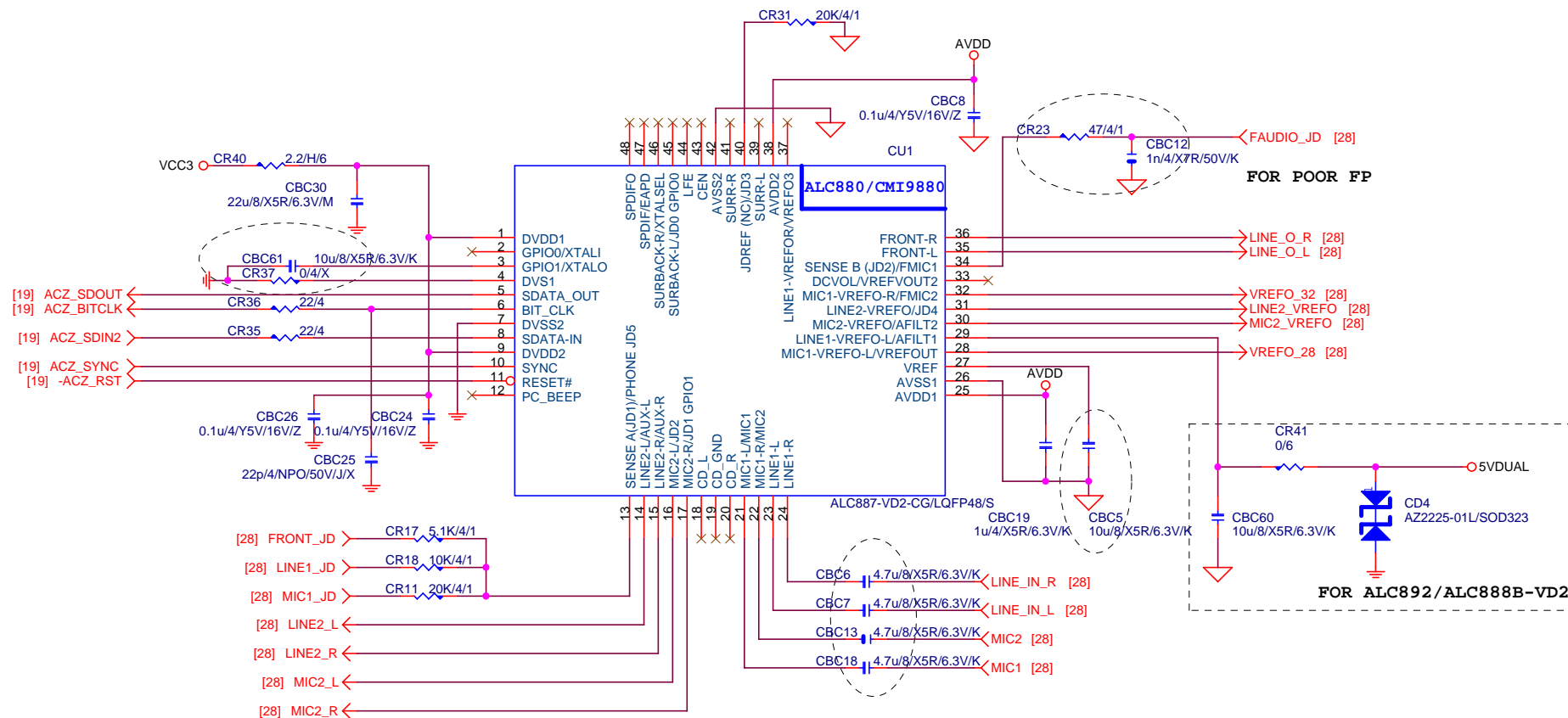
DUAL BIOS

(NEW HOLD# DESIGN)

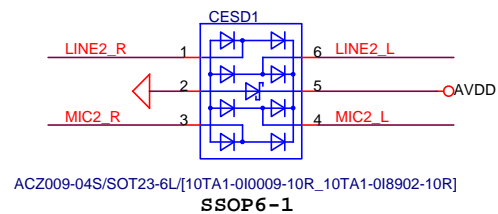


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HW-MONITOR/CI/KB/MS/BIOS			
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Can Support Amp Out



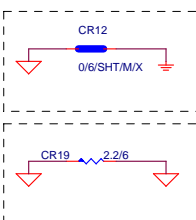
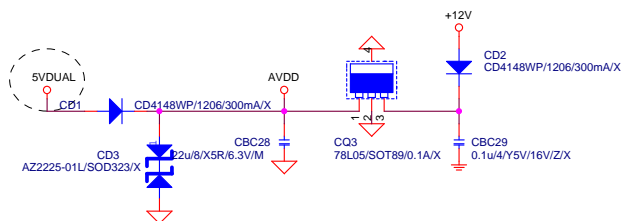
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SSOP6-1

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AZALIA ALC887-VD2				
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CODEC POWER/EMI PAD



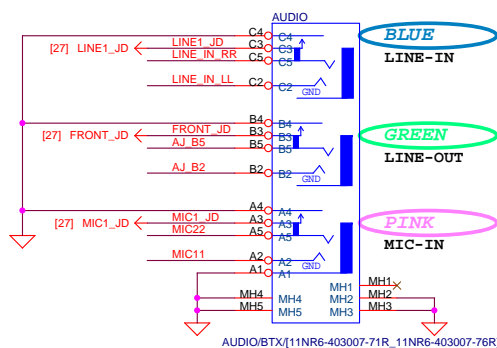
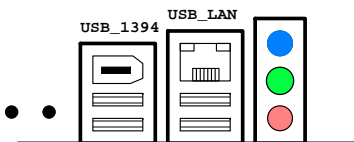
CD IN

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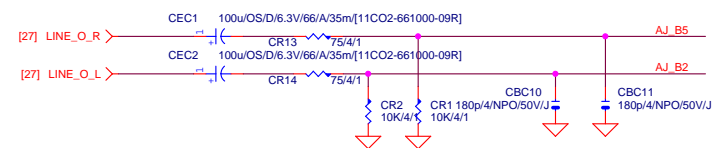
SPDIF IN

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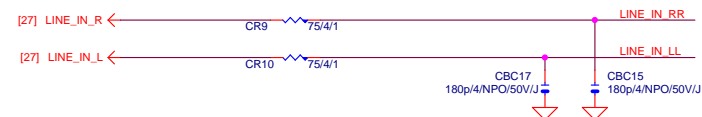
AZALIA JACK



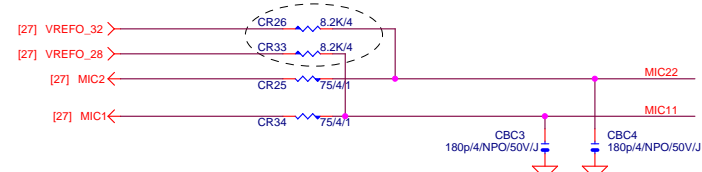
LINE-OUT



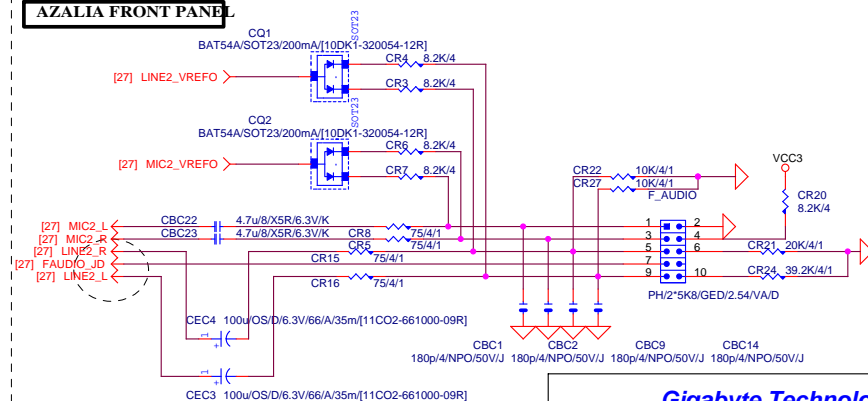
LINE-IN



MIC-IN

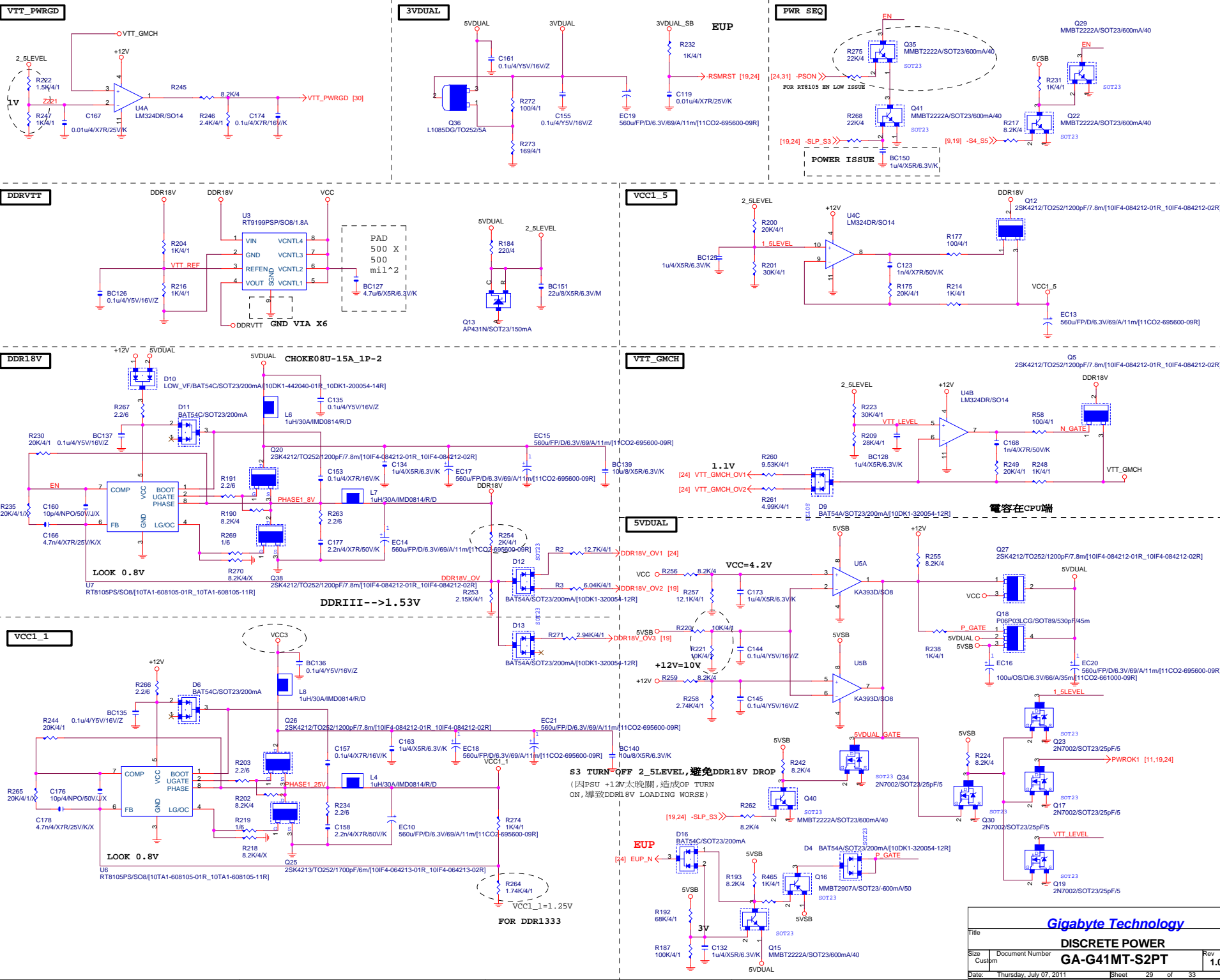


AZALIA FRONT PANEL

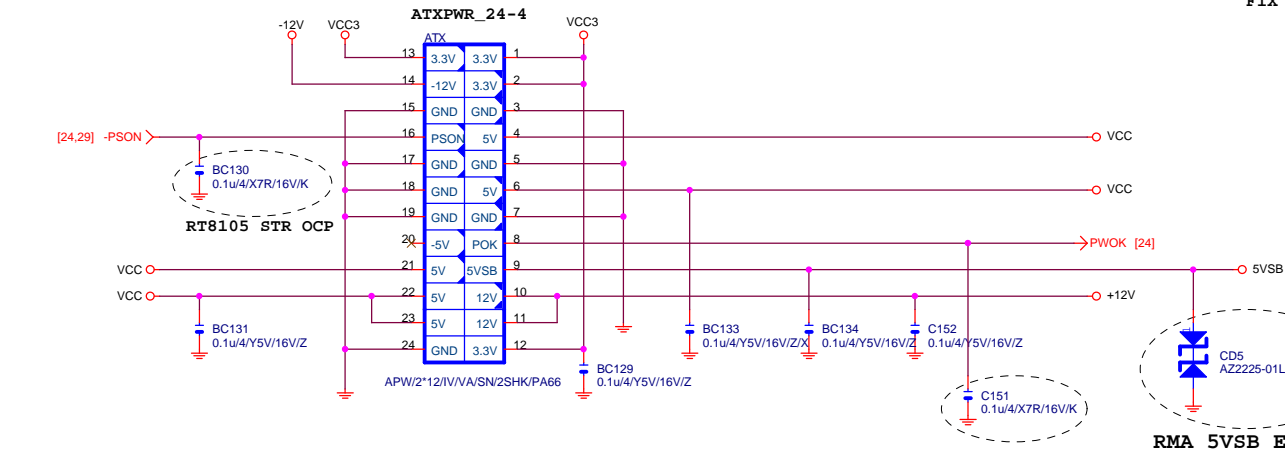


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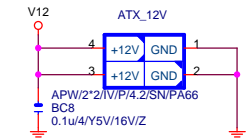
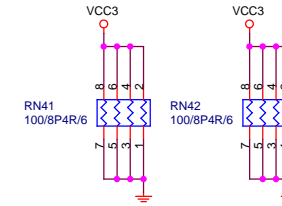
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AUDIO JACK			
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ATX POWER CONNECTOR

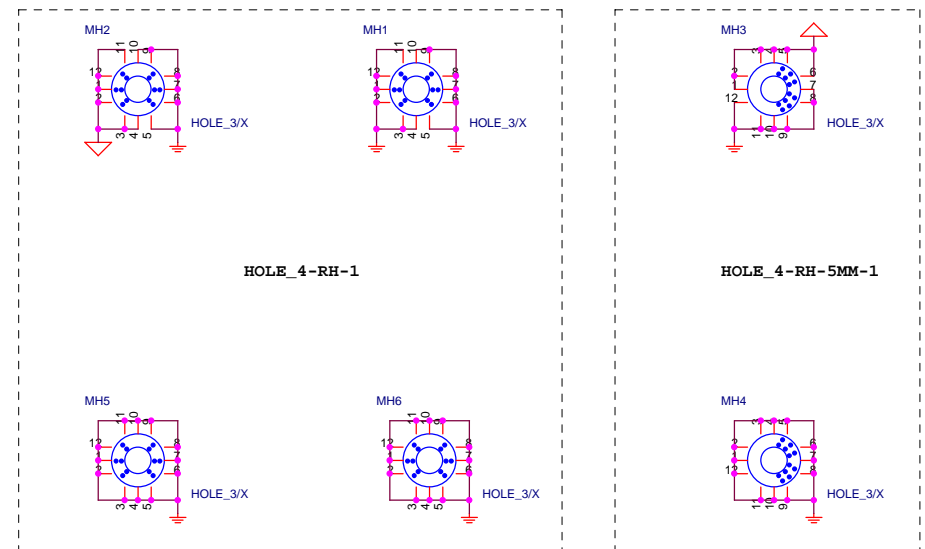
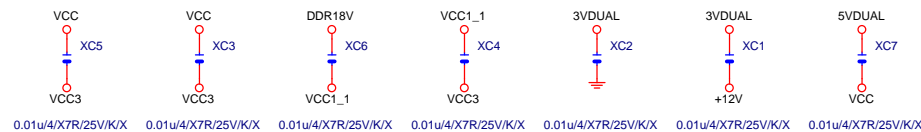
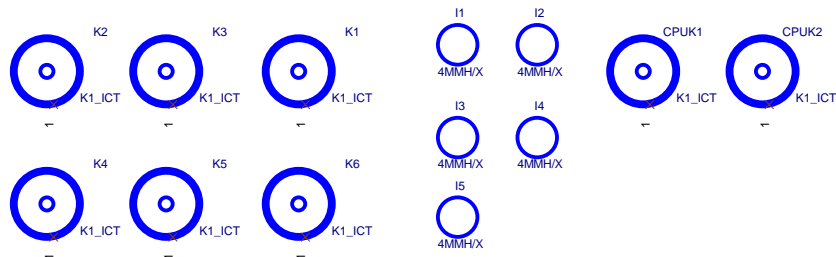
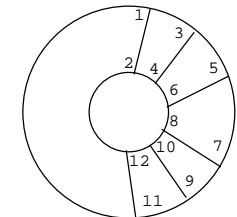
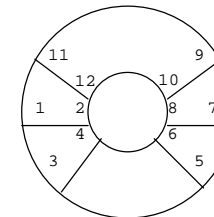


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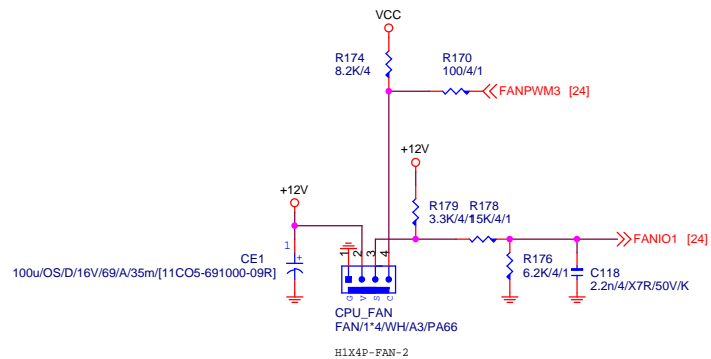
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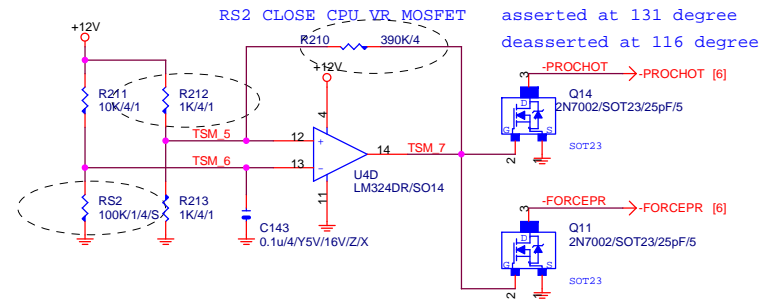


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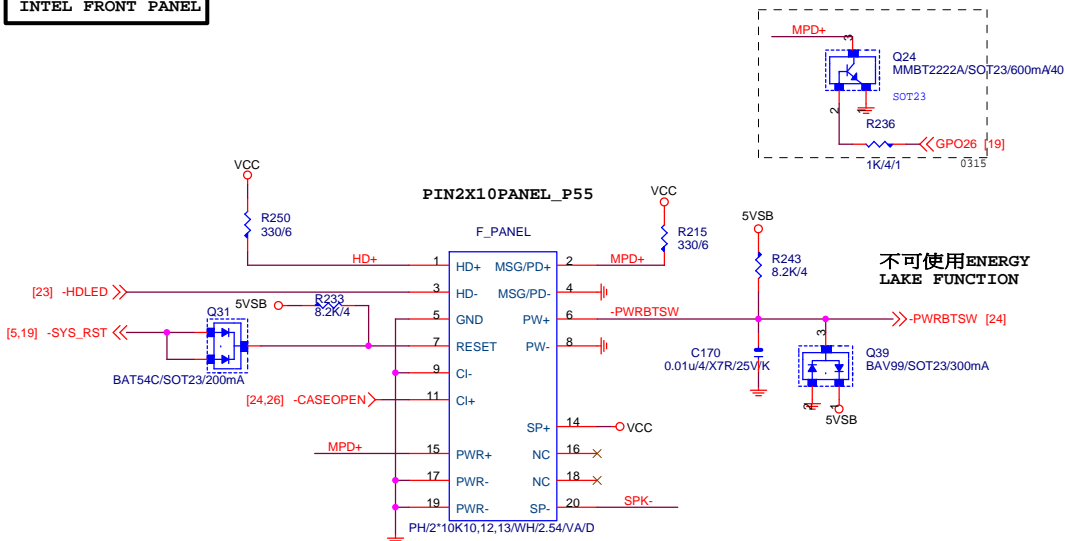
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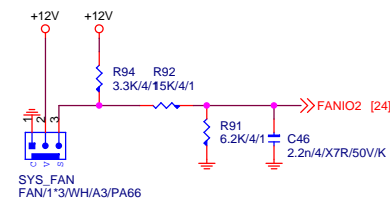
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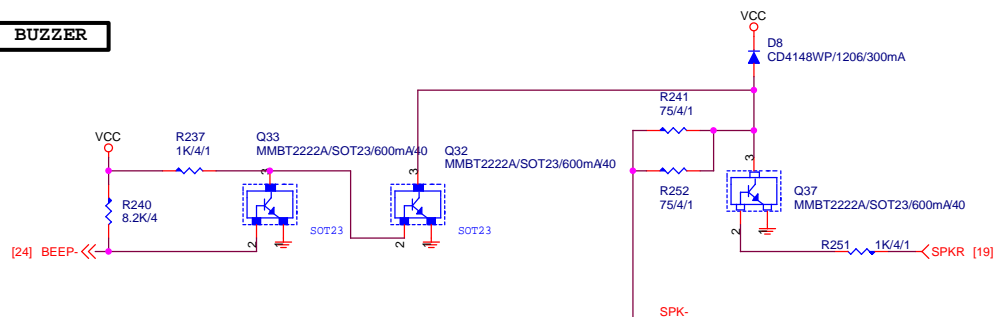
INTEL FRONT PANEL



SYS_FAN



BUZZER



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